**AUDIT COURSE**

**ELECTRONIC CIRCUITS 1: SIMULATION BASED STUDY**

**LAB 3**

Kindly update your name and roll no, once this document is shared with you

Time slot to complete your work is **40 MINUTES**

**Date: 10/8/2020**

Kindly upload your schematic & waveform images here, every 10 minutes, indicating your progress and intention to completion of WORK within time slot allotted

**Time slot allotted to you all for the completion of WEEK 2 DAY 1 is 40 MINUTES**

**Kindly upload your work (only circuit schematic & waveform in LTSpice) in the shared google doc between this time slot only.**

**Follow these instruction strictly:**

1, Start sharp ON TIME, by posting your name and roll no and **screenshot of your LT spice work screen ( time and date MUST BE VISIBLE)**

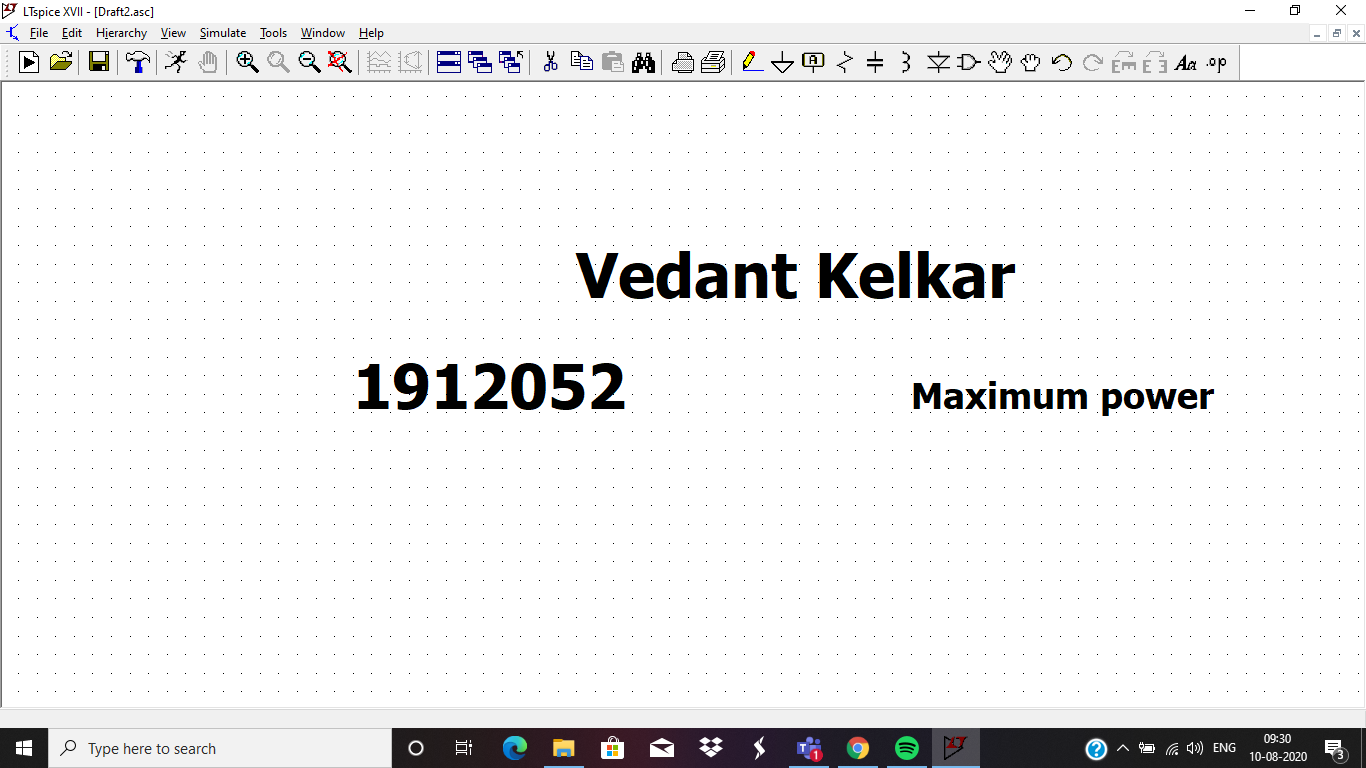
2. Upload your work every 10 minutes, i.e LT spice work screen

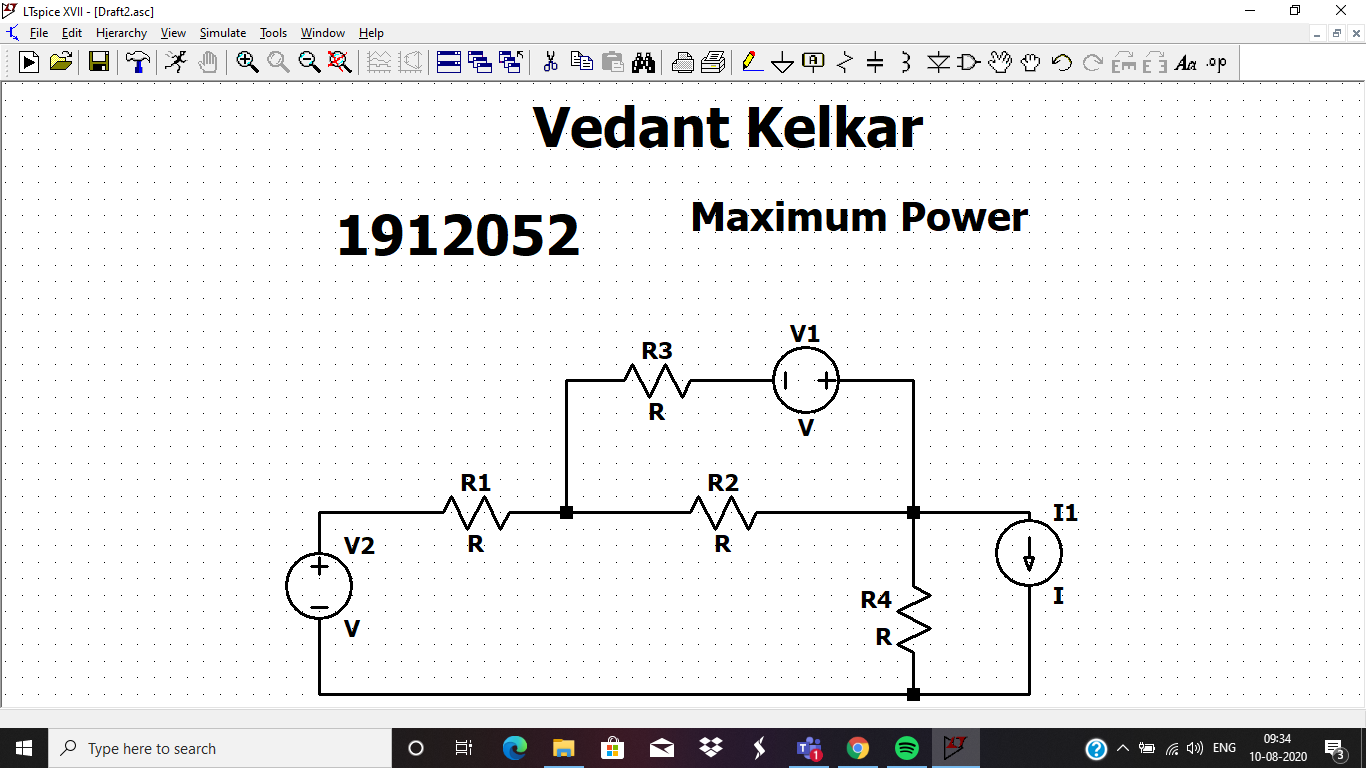
3. This means you will upload LT spice work screen 4 times during this time slot.

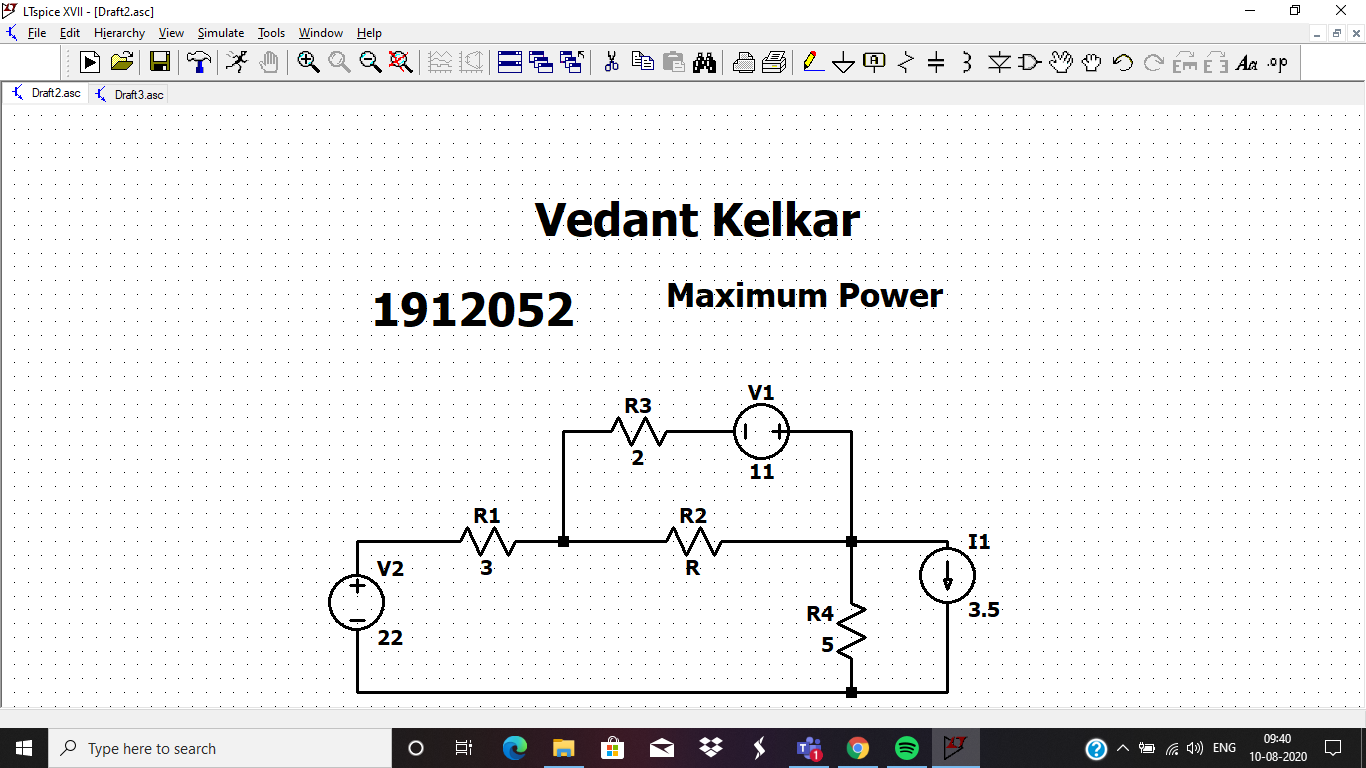
4. Point 3 indicates your readiness and presences for completion of WEEK 2 DAY 1

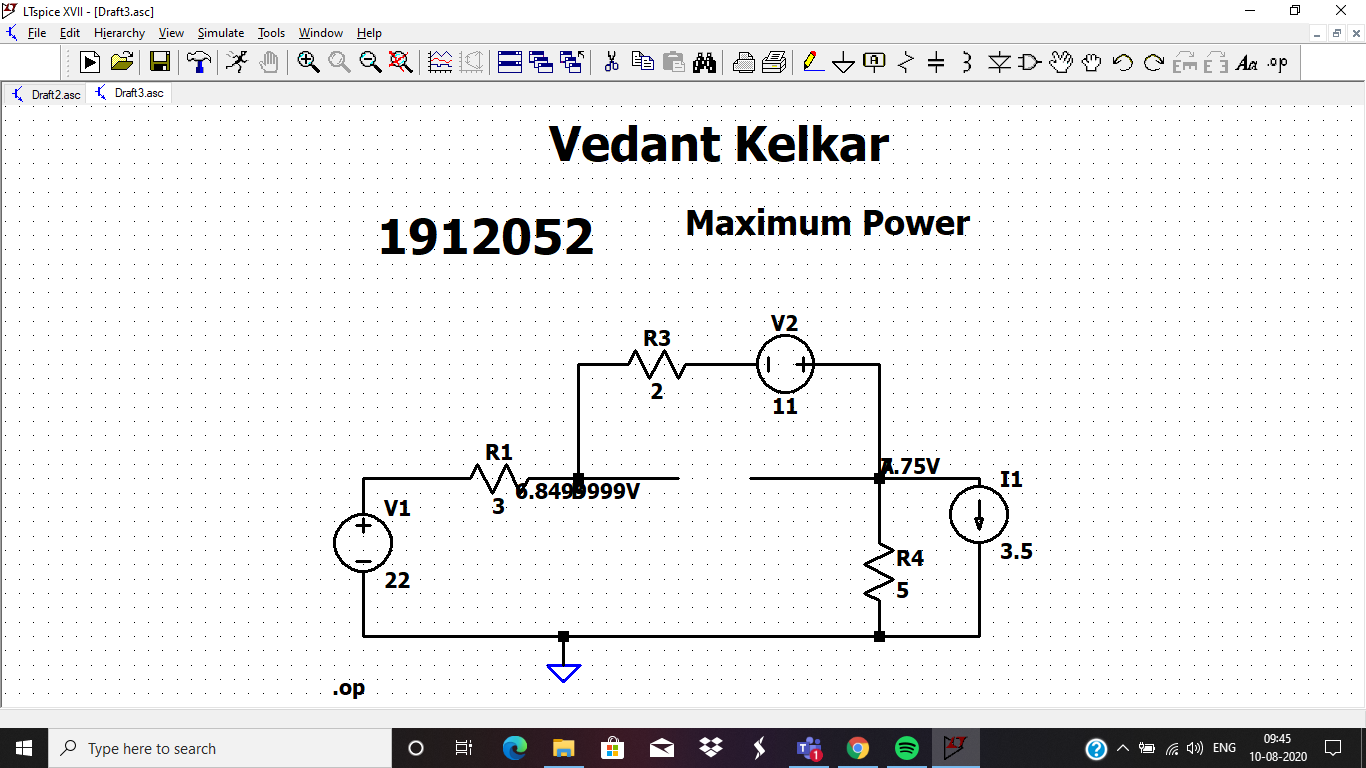
**You are entitled for 1 CREDIT per Lab only if you follow above instruction to the details**

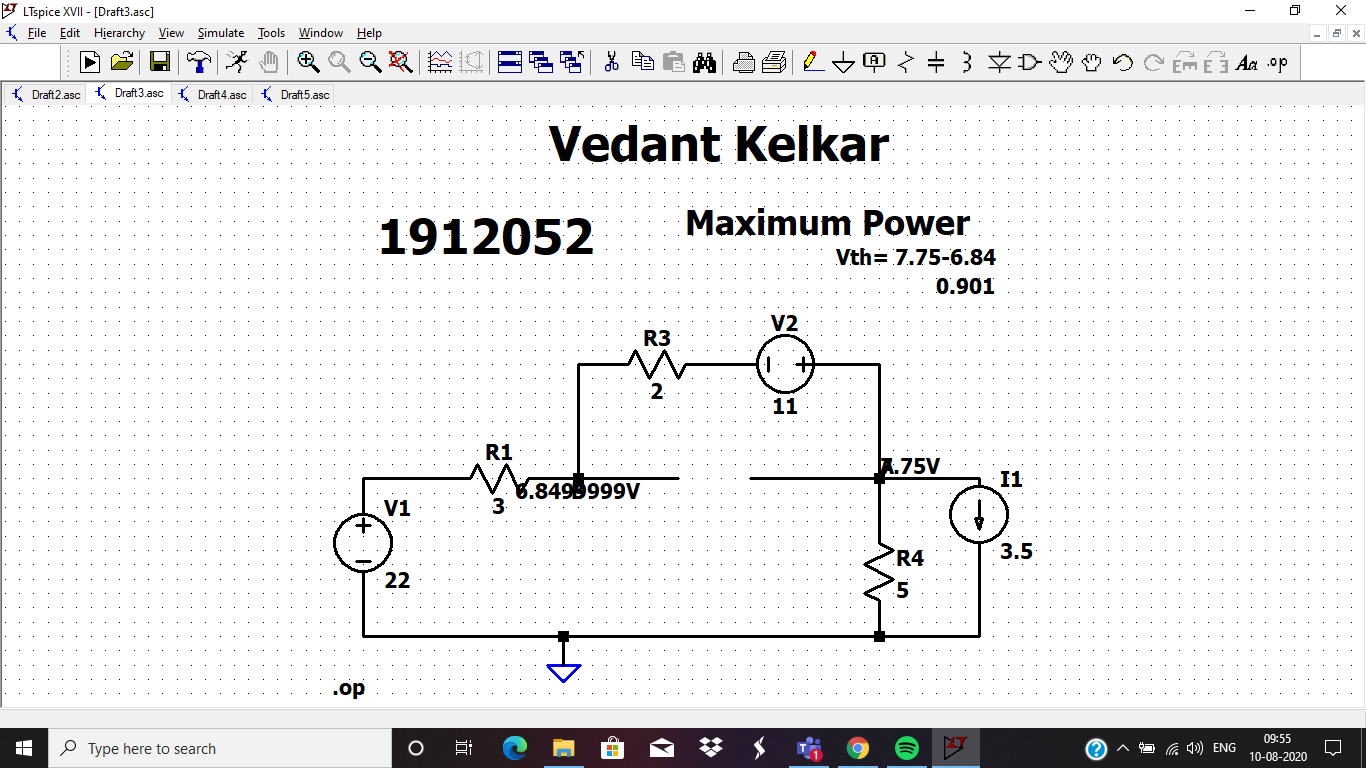
**STUDENTS WORK AREA STARTS HERE**

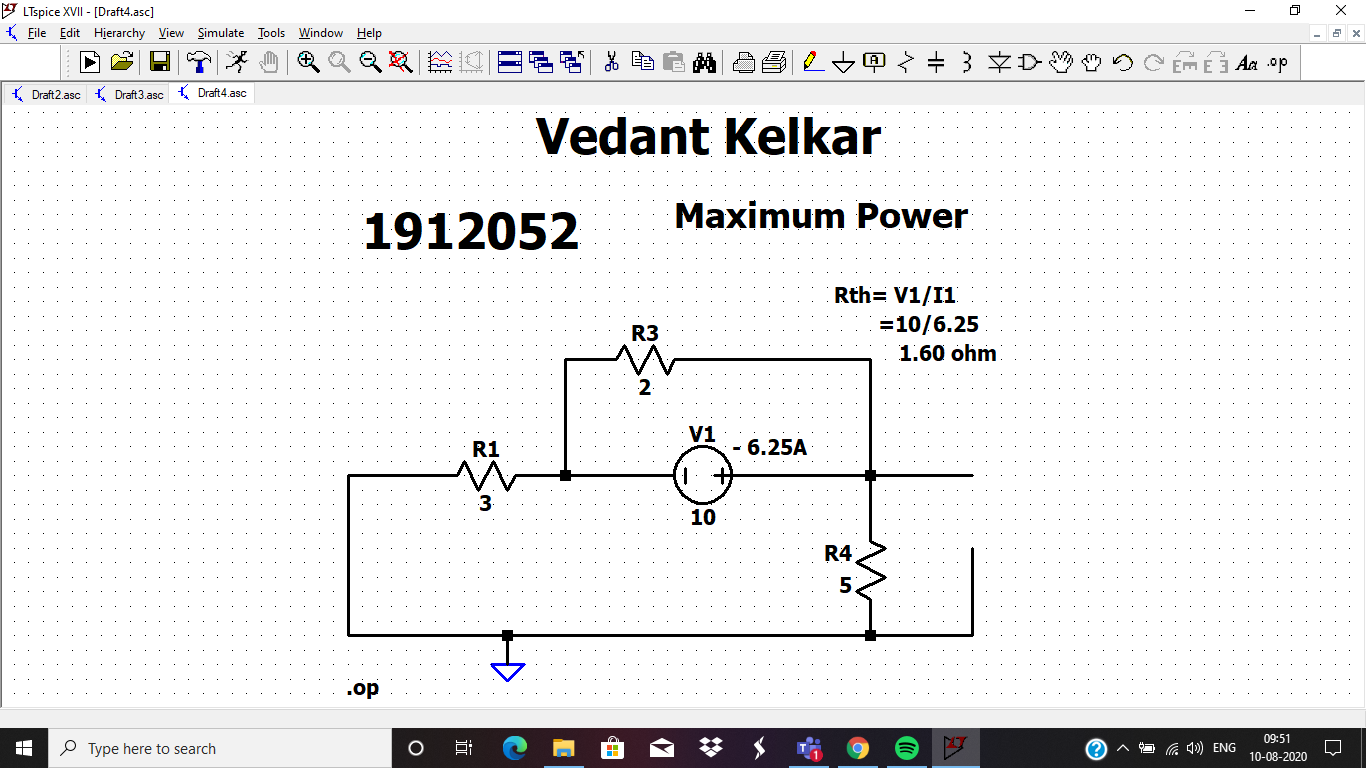
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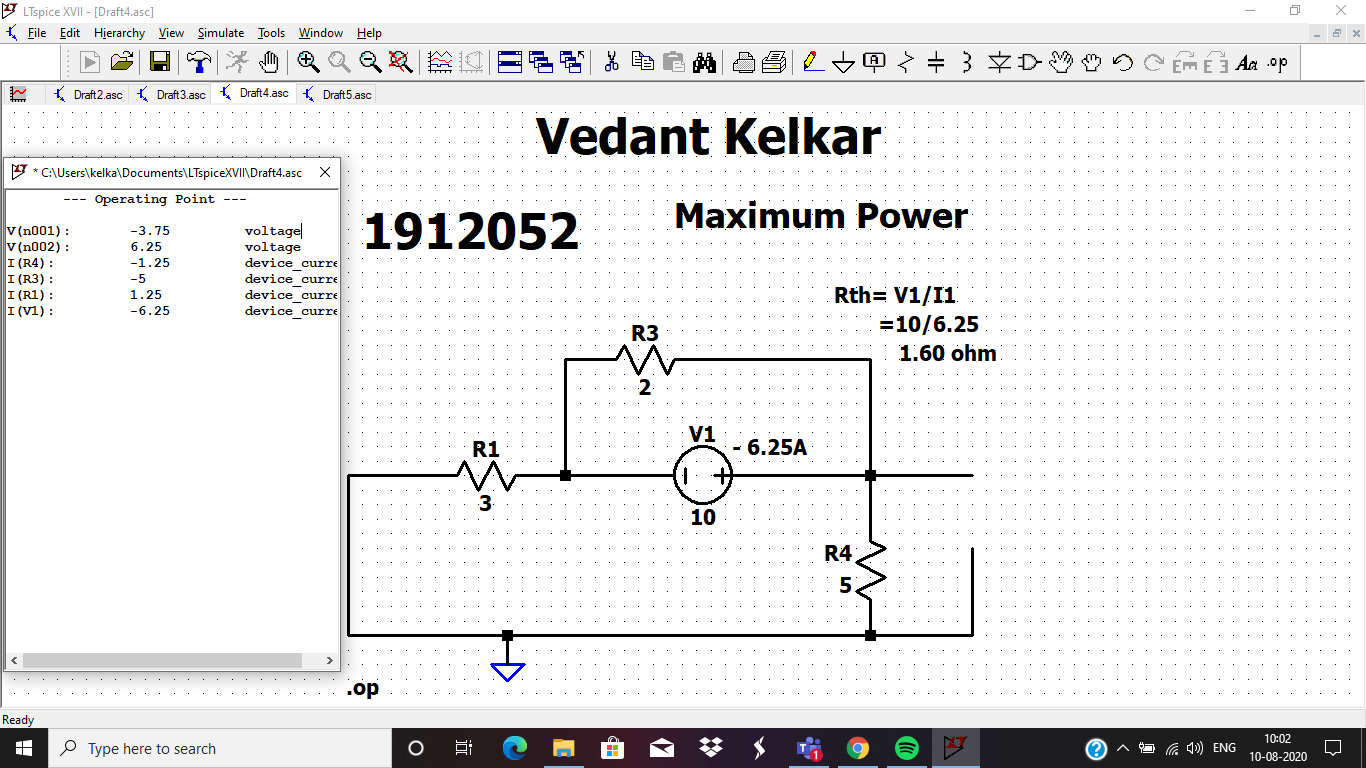
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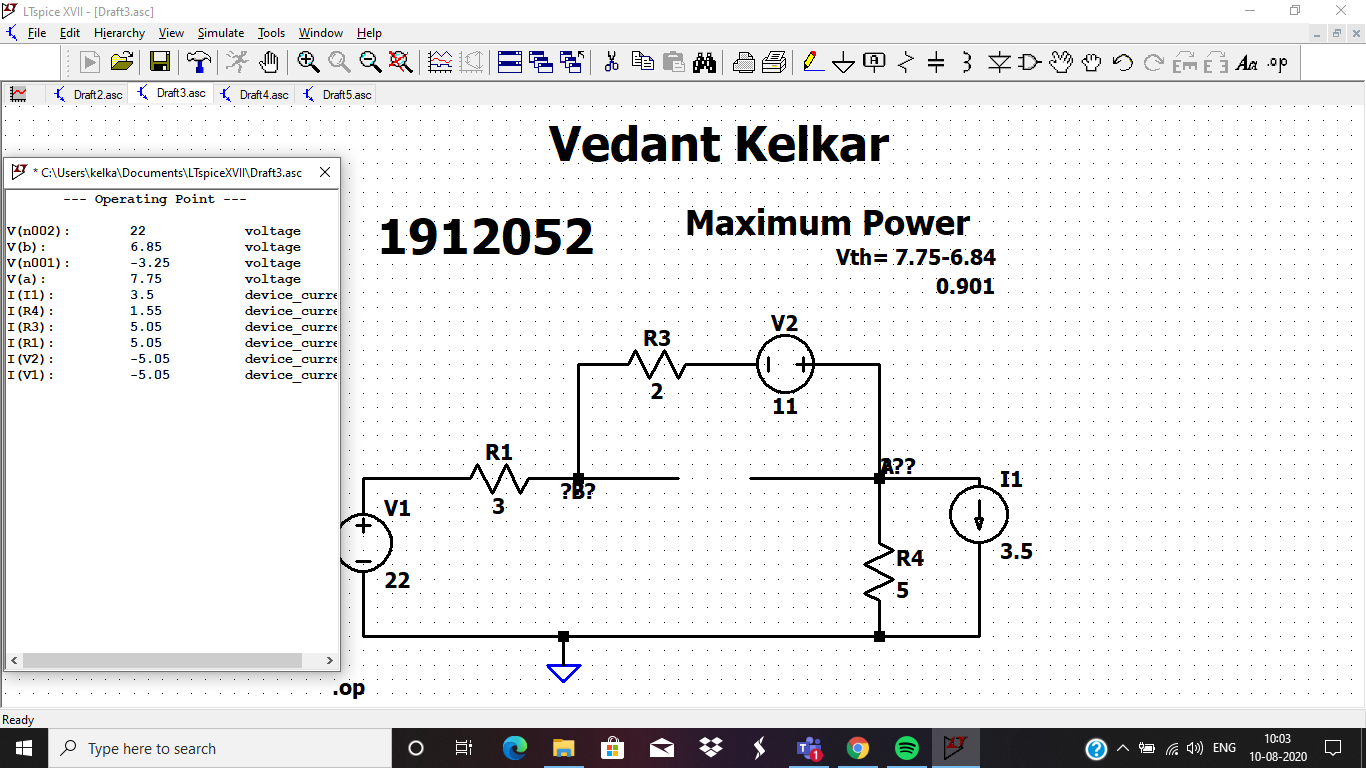
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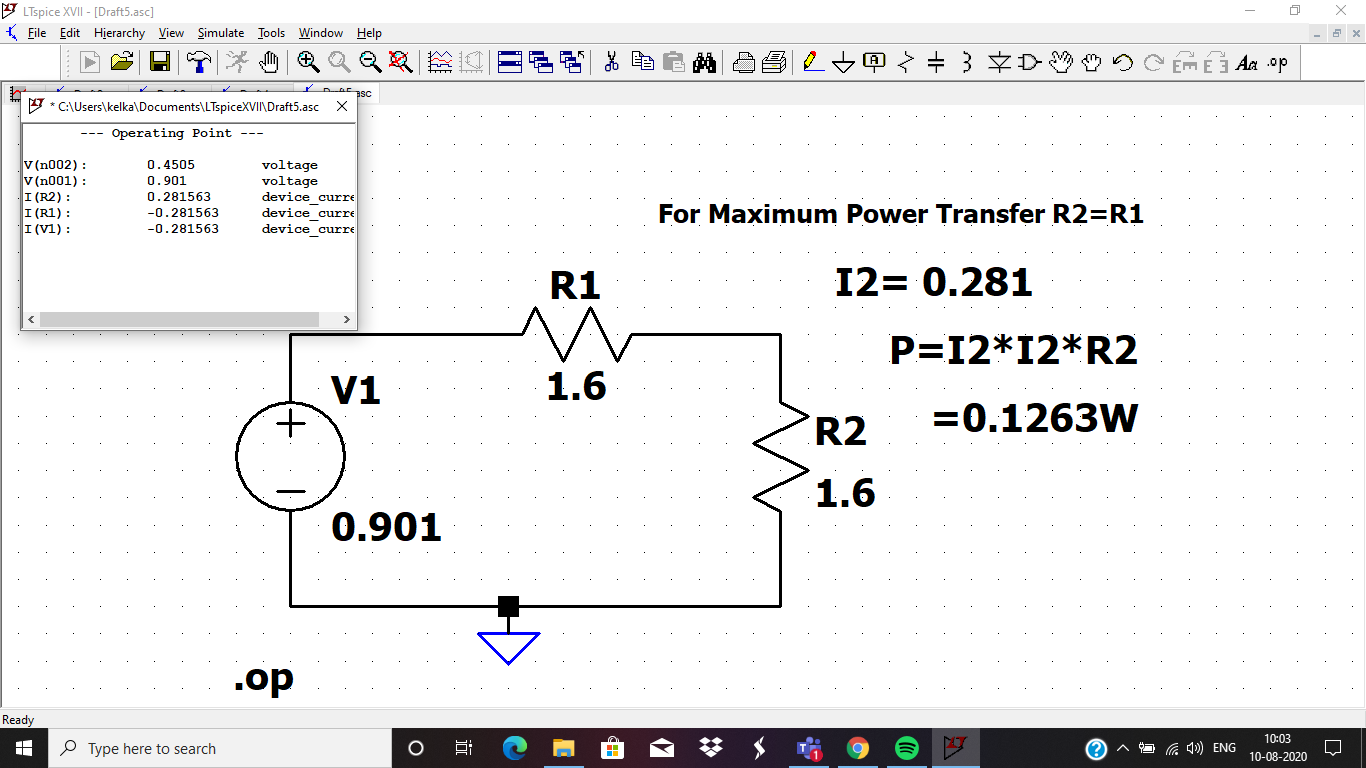
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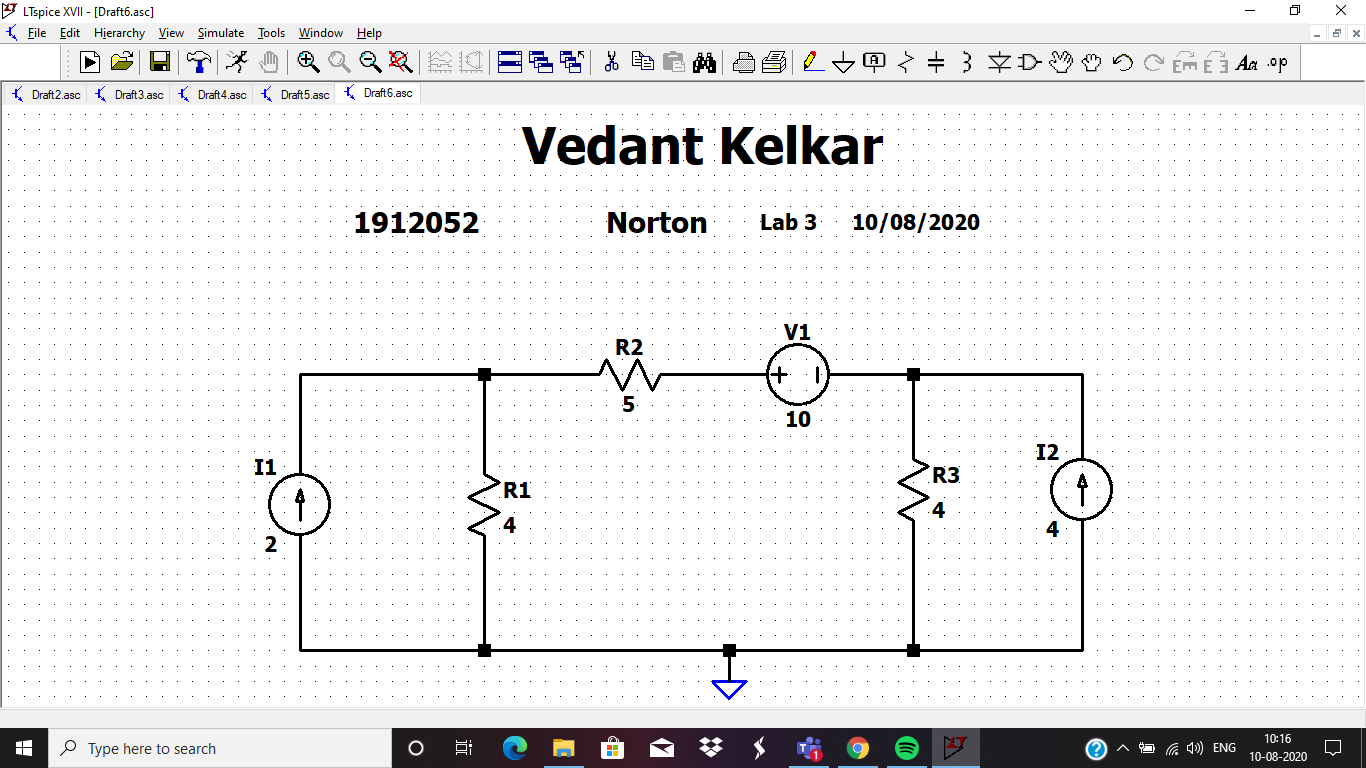
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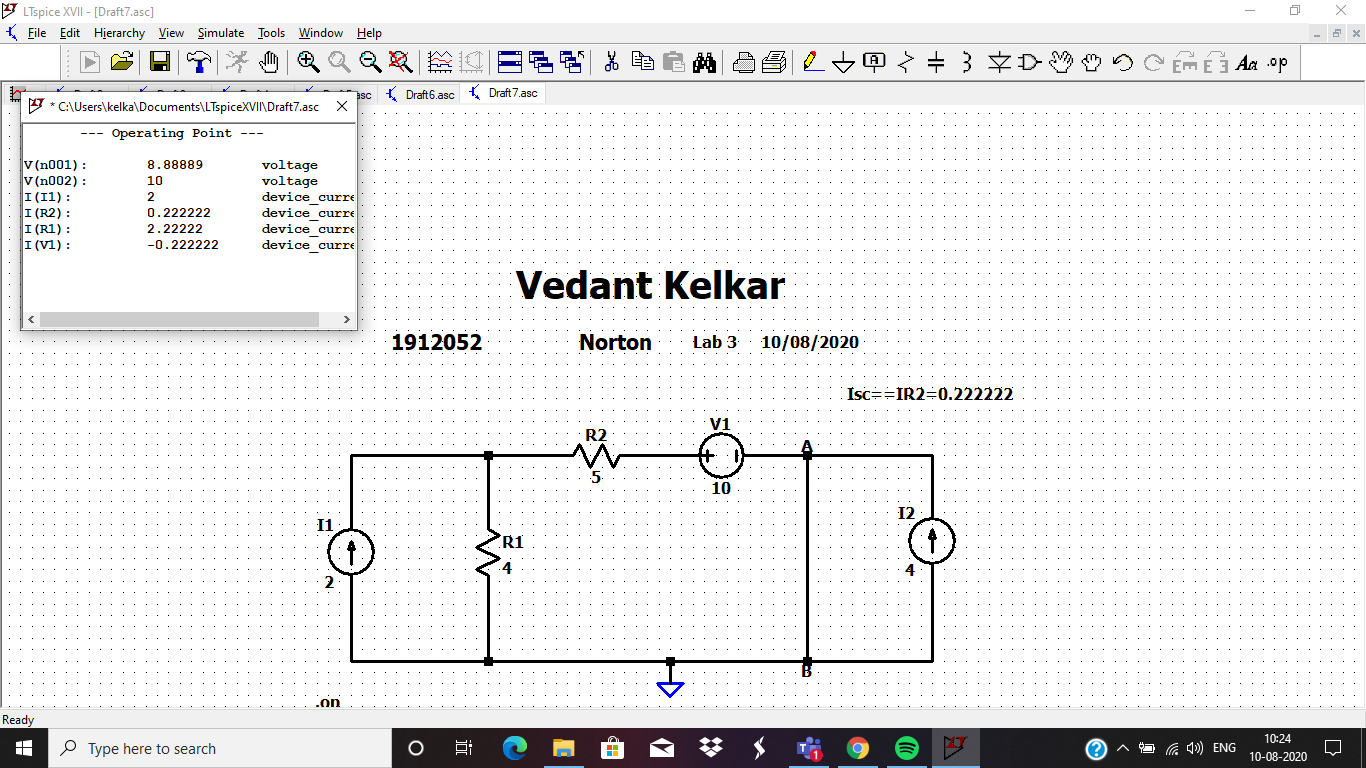
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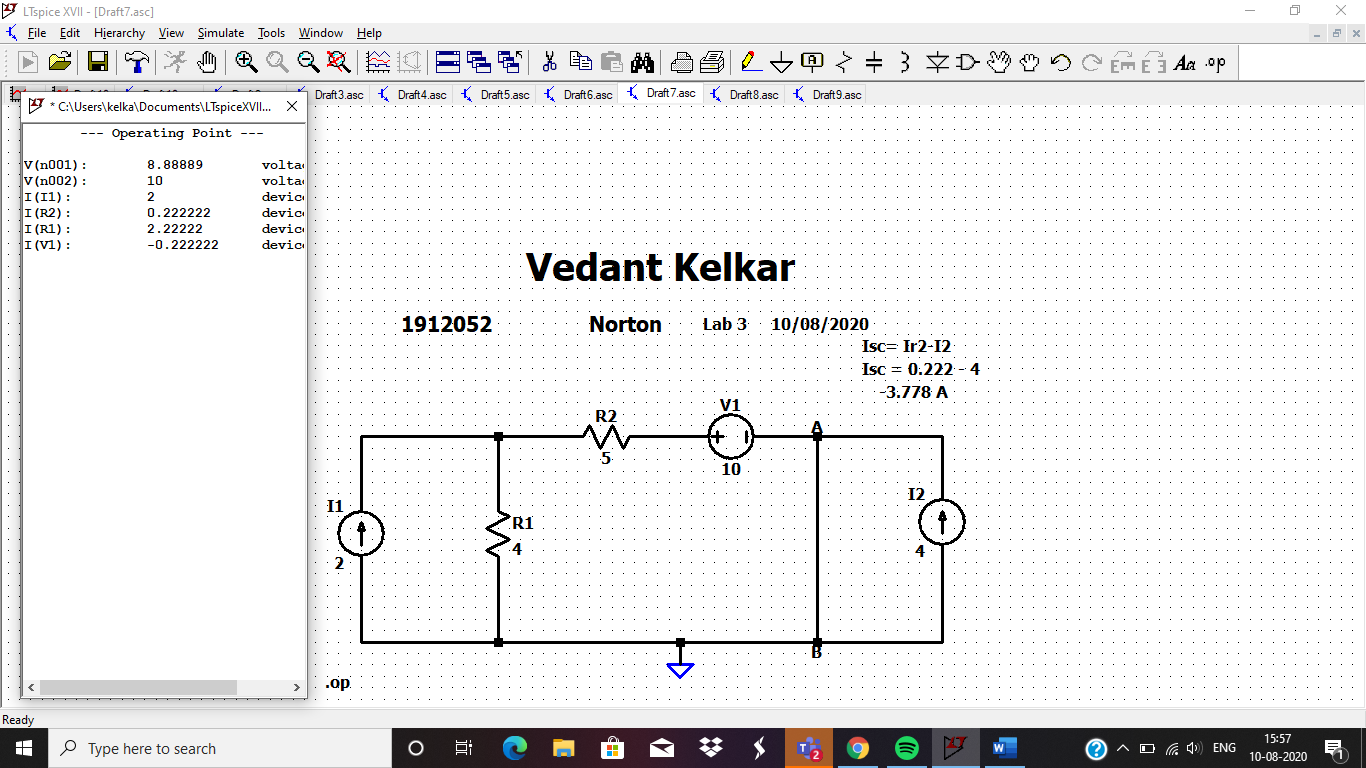
**Nortons Theorem**

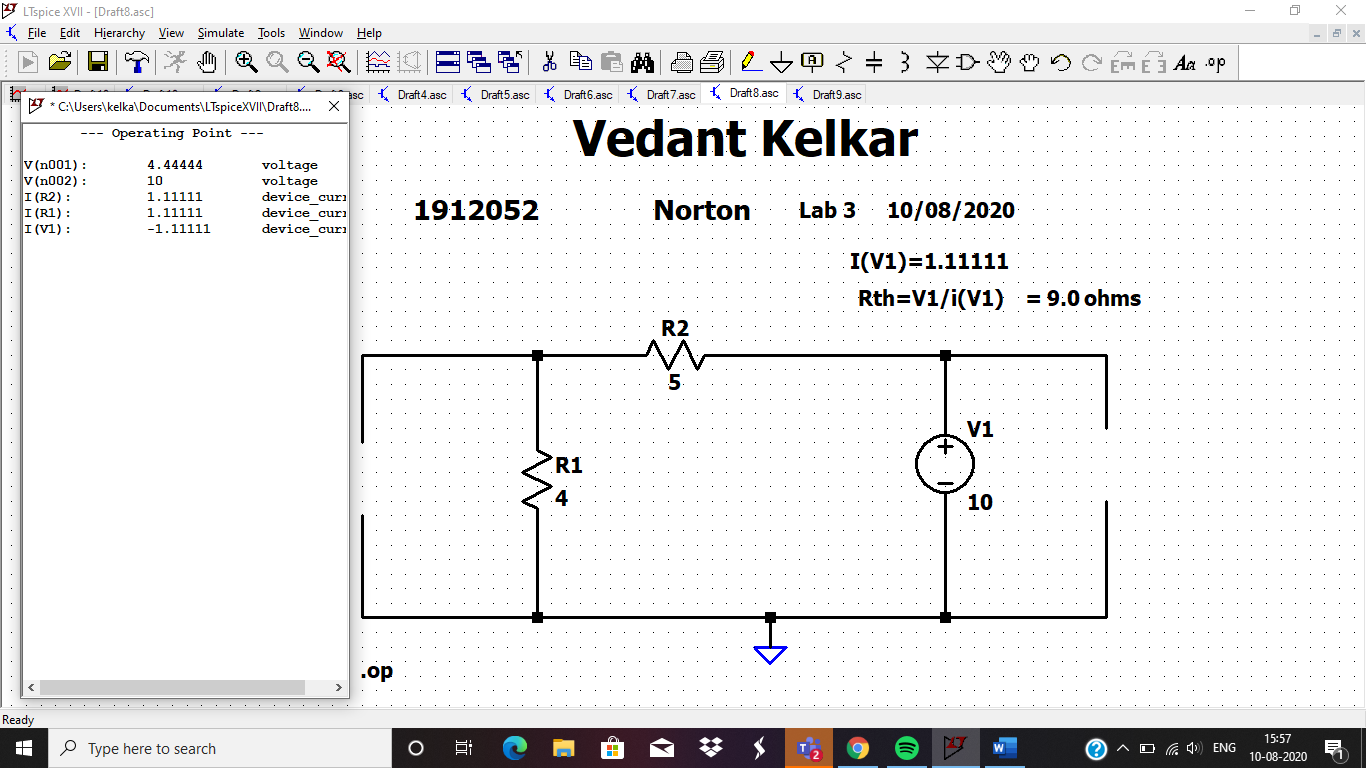
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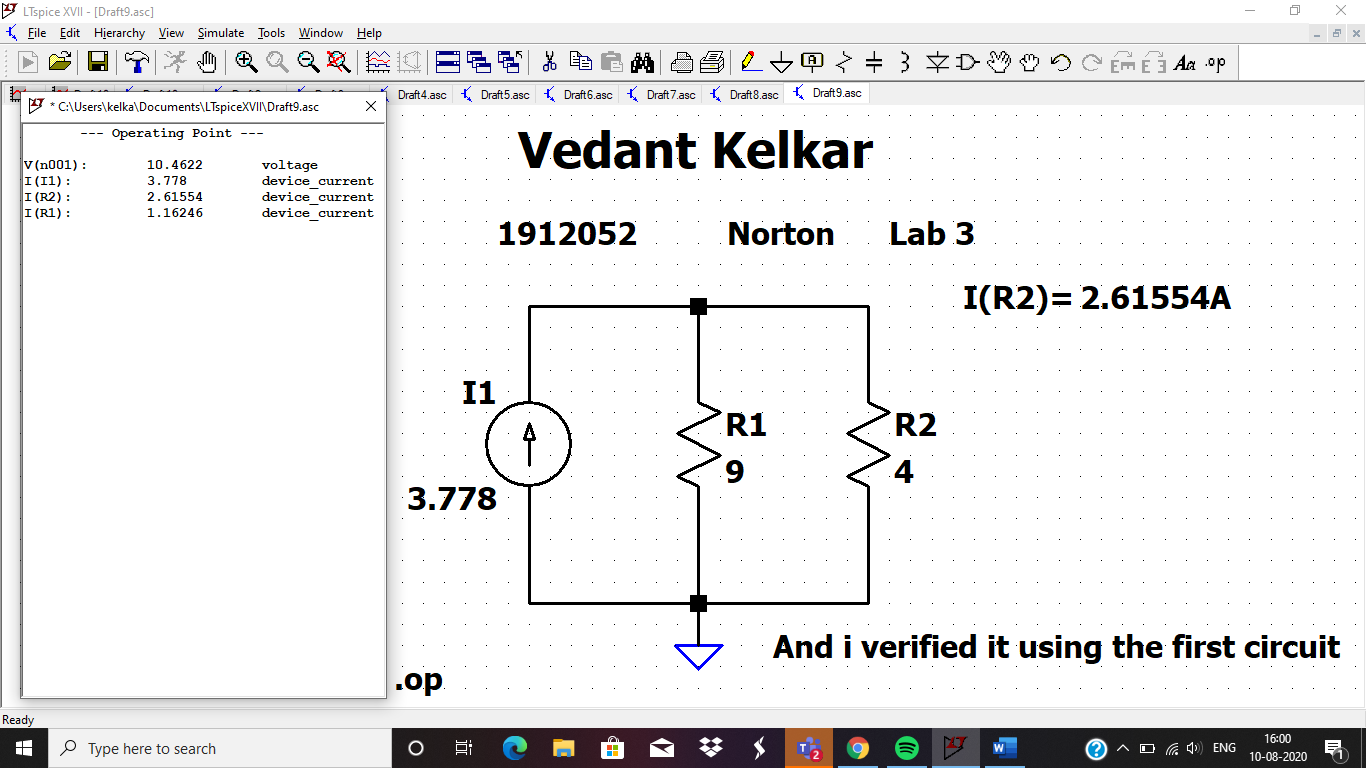
**THIS ONE IS WRONG**

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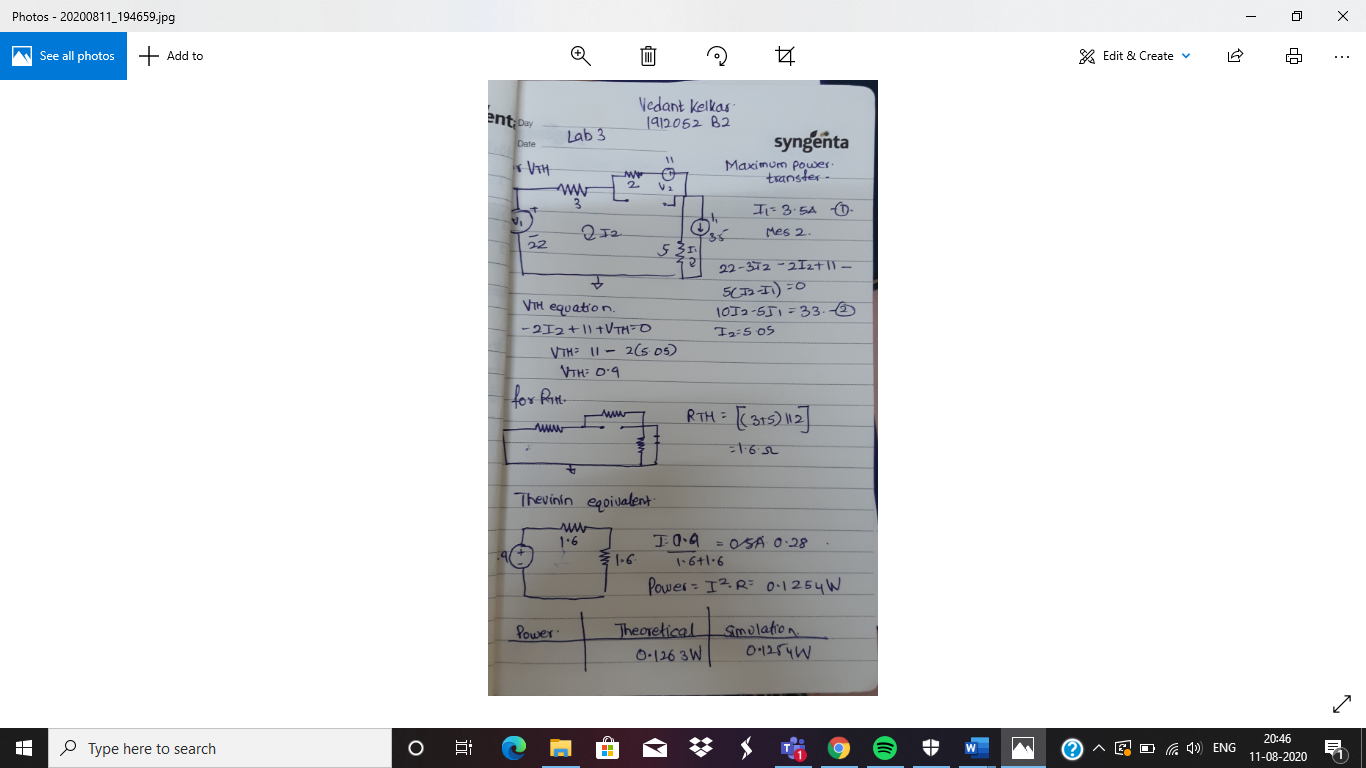
**CORRECTED IMAGE HERE**

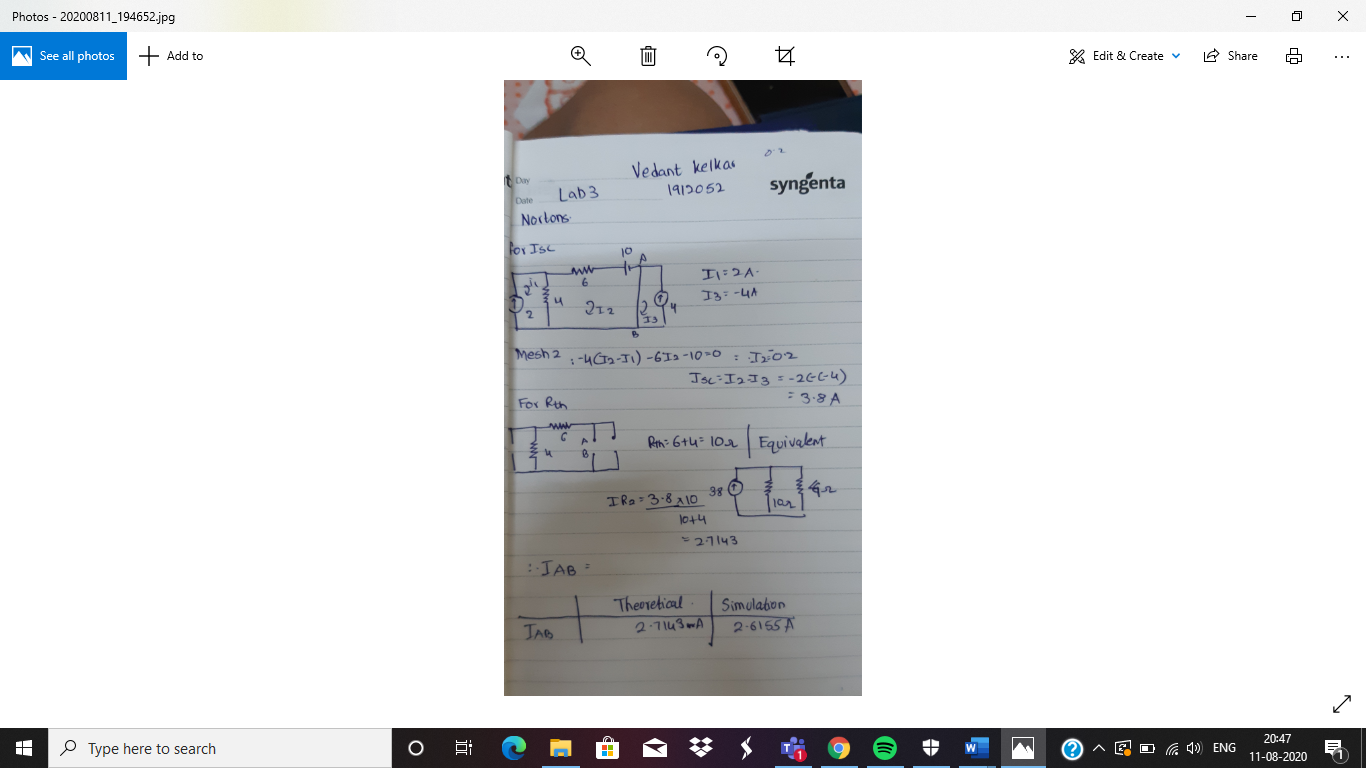
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**THEORETICAL**

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**LAB 3 is approved: Inderjit Singh Dhanjal**